Maximizing IC Performance

NT5706

## 1. DESCRIPTION

MT5706 is a System on Chip (SoC) for magnetic induction based wireless power receiver.

It is capable of wireless charging for up to 30W(12V/2.5A) and can be configured as a wireless charging transmitter to charge other receivers. It is fully compliant with WPC Qi Specification (Version 2.0) of BPP (Baseline Power Profile), EPP (Extended Power Profile) and MPP (Magnetic Power Profile) and also supports various proprietary fast charging protocols used by major smart phone OEM's.

MT5706 has a very high overall AC to DC conversion efficiency (up to 97%), thanks to the optimized and adaptive full synchronous rectifier control, very small  $R_{DSON}$  of power MOSFET, and extremely low bias current are achieved.

The AC input can be converted to fully programmable DC output voltage due to the magnetic induction charging technology.

Designed with ARM Cortex M0 processor, integrated with optimized and adaptive full synchronous rectifier control and special LDO, the chip achieves high efficiency, ultra-low bias current and very small power MOSFET R<sub>DSON</sub>.

MT5706 is embedded with various protection features, such as FOD, over-voltage, overcurrent and over-temperature protection which guarantee the system reliability.

## 2. APPLICATIONS

- Smartphones and wearable devices with high integration and small form factor
- Rx function for power banks where they can be wirelessly charged
- Other wireless power applications

## 3. FEATURES

- Power delivery: up to 30W(12V/2.5A)
- Power delivery in Tx mode: up to 5W
- Fully programmable output voltage and current limit
- Embedded with ARM Cortex M0 processor with 8KB SRAM and 32KB MTP
- Supports half-wave mode (default) and fullwave mode
- 85KHz~2MHz wide wireless charging frequency
- Supports 1.8V I/O and 1.2V I/O
- Embedded with various protection features:
  - over-voltage protection (OVP)
  - over-current protection (OCP)
  - short-circuit protection (SCP)
  - over-temperature protection (OTP)
  - foreign object detection (FOD)
- Innovative output LDO with output clamping and fast response to line and load transient
- Qi 2.0 compliant and proprietary communication protocols support with hardware ASK modulation
- Independent I<sup>2</sup>C slave interface with additional GPIO's
- Halogen free and RoHS compliant
- 2.10mm x 3.30mm (5 x 8 ball array) 40-WLCSP



## 4. TYPICAL APPLICATION CIRCUIT



Figure 1 Typical Application Circuit