

ABSOLUTE MAXIMUM RATINGS

VDD Pin Voltage	-0.3V to VDD Clamp
Drain Pin Voltage	-0.3V to 550V
COMP/CS/DSEN Pins Voltage	-0.3V to 6V
Lead Temperature (soldering, 10 sec.)	260°C
P _{DMAX} (maximum power consumption)	0.8W(SOP8);
Storage Temperature	-55°C to 150°C

Recommended operating conditions

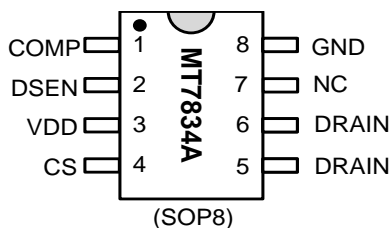
Supply voltage	9V to 24V
Operating Temperature (Environment)	-40°C to 105°C
Output Power	≤ 18W @ 90VAC~264VAC ≤ 24W @ 176VAC~264VAC

Thermal resistance^①

Junction to ambient (R _{θJA})	128°C/W
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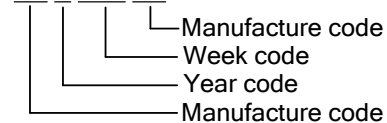
Note:

① R_{θJA} is measured in the natural convection at TA = 25°C on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Test condition: Device mounted on 2" X 2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane.

PIN CONFIGURATIONS

Chip Mark

MT7834A

XX Y WW XX


PIN DESCRIPTION

Name	Pin No.	Description
COMP	1	Internal EA's output pin. Connect a capacitor to ground for frequency compensation.
DSEN	2	Feedback pin for inductor zero current crossing detection.
VDD	3	Power Supply pin.
CS	4	Source of internal MOSFET, and Current Sense pin.
DRAIN	5,6	Drain of internal MOSFET.
NC	7	No connection pin.
GND	8	Ground pin.